

KVR266X72C2/512

512MB 64M x 72-Bit DDR266

CL2 ECC 184-Pin DIMM

DESCRIPTION

This document describes ValueRAM's 64M x 72-bit (512MB) DDR266 CL2 SDRAM (Synchronous DRAM) ECC memory module. The components on this module include eighteen 32M x 8-bit DDR266 SDRAM in TSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.5V. The electrical and mechanical specifications are as follows:

SPECIFICATIONS

Clock Cycle Time (tCK)	7.5ns (min.) / 12ns (max.)
Row Cycle Time (tRC)	65ns (min.)
Refresh Row Cycle Time (tRFC)	75ns (min.)
Row Active Time (tRAS)	45ns (min.) / 120,000ns (max.)
Single Power Supply of	+2.5V (+/- .25V)
Power (IDD0)	3.262 W (operating)
UL Rating	94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here.

Figure 1: KVR266X72C2/512 184-Pin DDR SDRAM Module

