

KVR333X64SC25/1G

1GB 128M x 64-Bit DDR333

CL2.5 200-Pin SODIMM

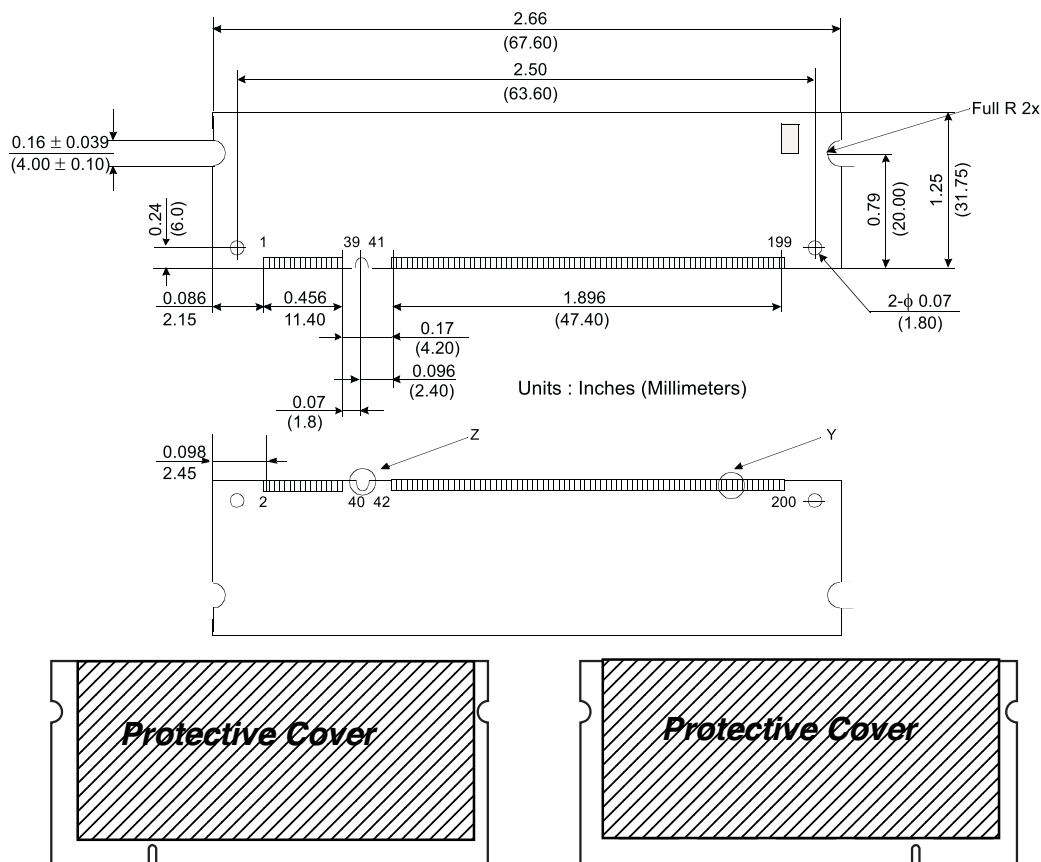
DESCRIPTION

This document describes ValueRAM's 128M x 64-bit (1GB) DDR333 CL2.5 SDRAM (Synchronous DRAM) memory module. The components on this module include sixteen 64M x 8-bit (16M x 8-bit x 4 Bank) DDR333 SDRAM sealed in TCP packages. This 200-pin DIMM uses gold contact fingers and requires +2.5V. The electrical and mechanical specifications are as follows:

SPECIFICATIONS

Clock Cycle Time (tCK)	6ns (min.) / 12ns (max.)
Row Cycle Time (tRC)	60ns (min.)
Refresh Row Cycle Time (tRFC)	72ns (min.)
Row Active Time (tRAS)	42ns (min.) / 70,000ns (max.)
Single Power Supply of	+2.5V (+/- .2V)
Operating (IDD0)	4.400 W
UL Rating	94 V - 0

Figure 1: KVR333X64SC25/1G 200-Pin SODIMM

Tolerances : ± 0.006 (.15) unless otherwise specified